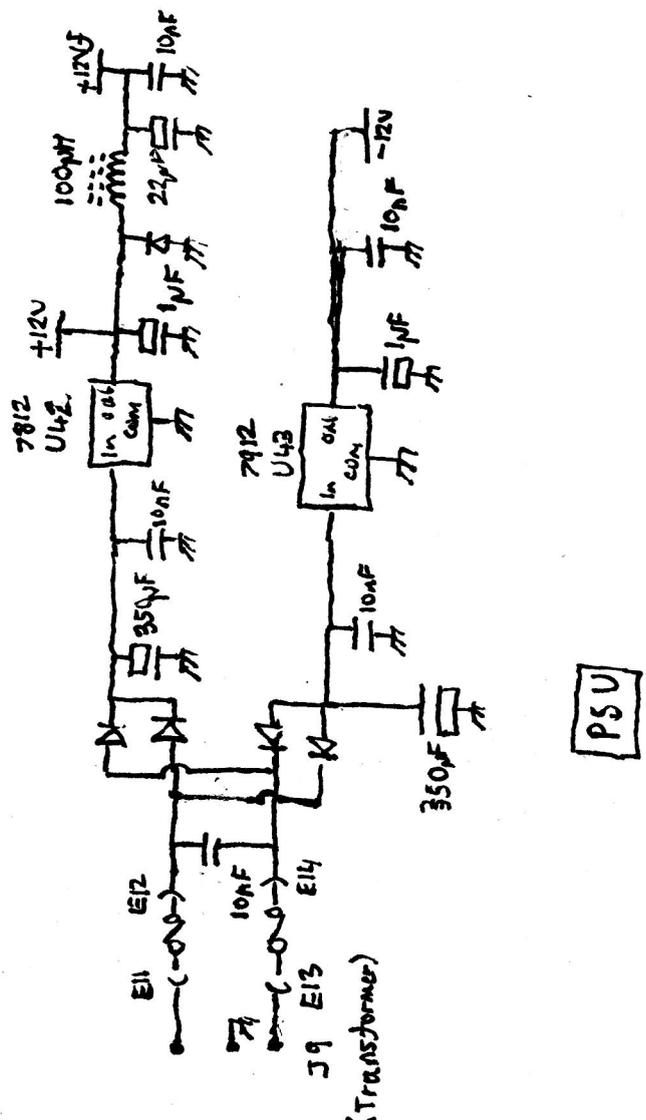
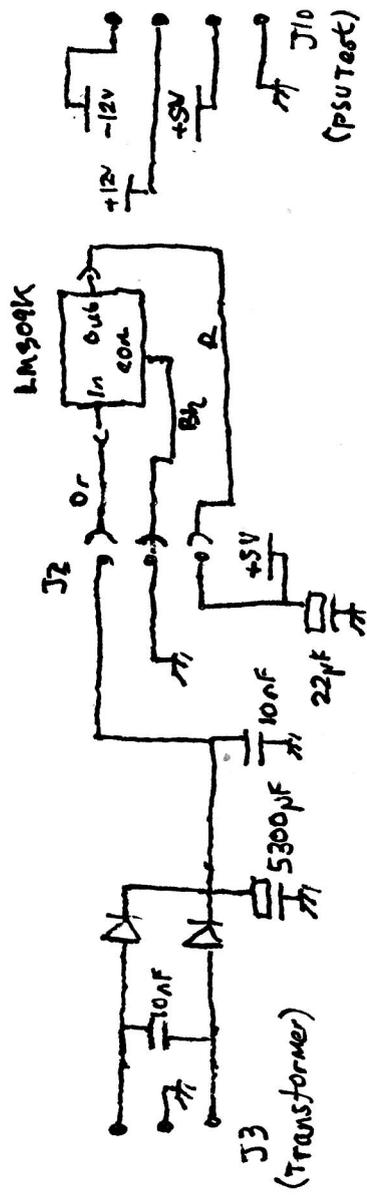
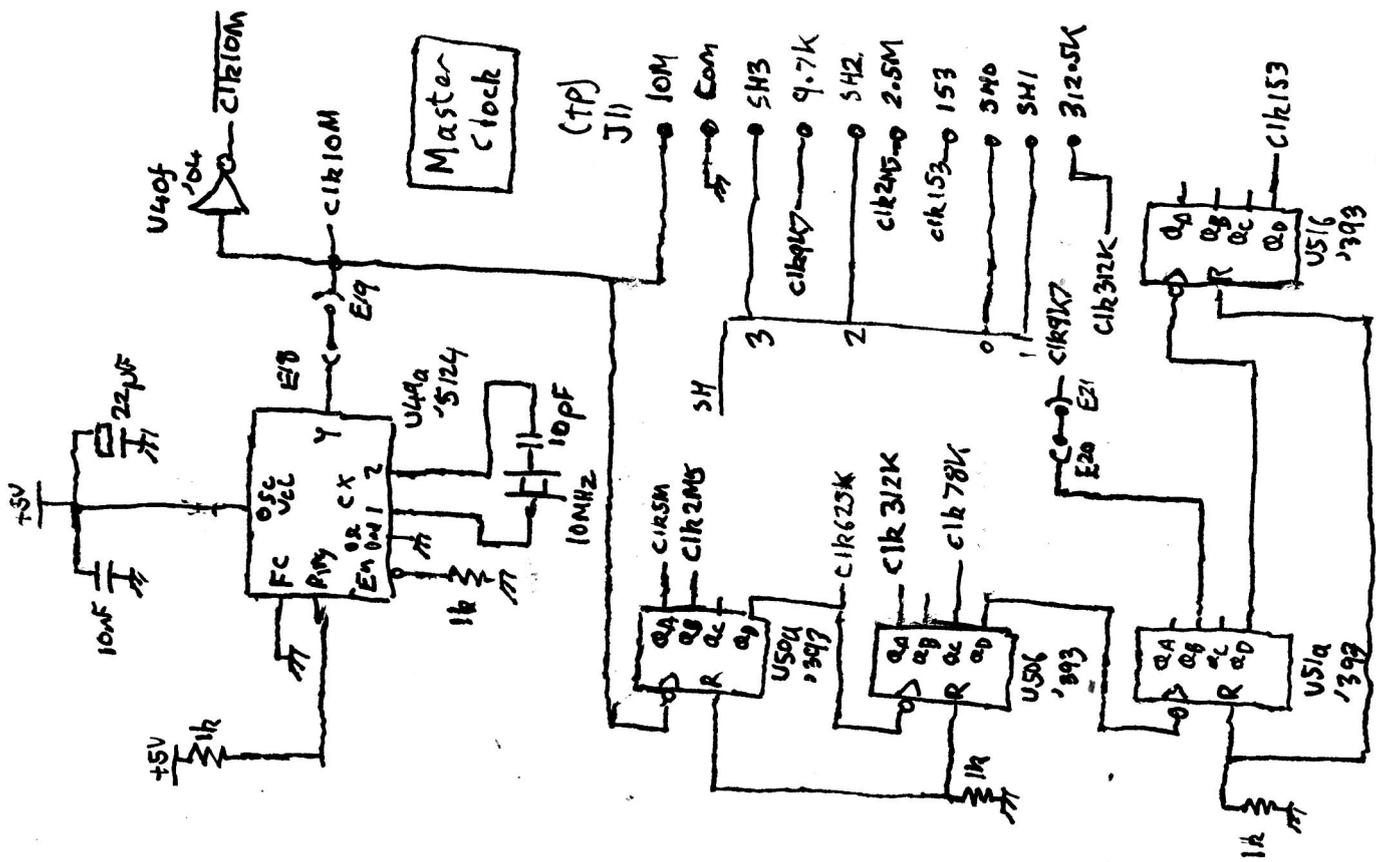


There are 2 versions of the main logic board used in this digitising tablet : The older one (believed to be 09111-66501) uses the Intel 8291 GPIB interface with 8293 buffers; the later one uses the Texas Instruments TMS9914 GPIB interface with 75160/75161 buffers. There are other differences, but that's the easy way to tell them apart

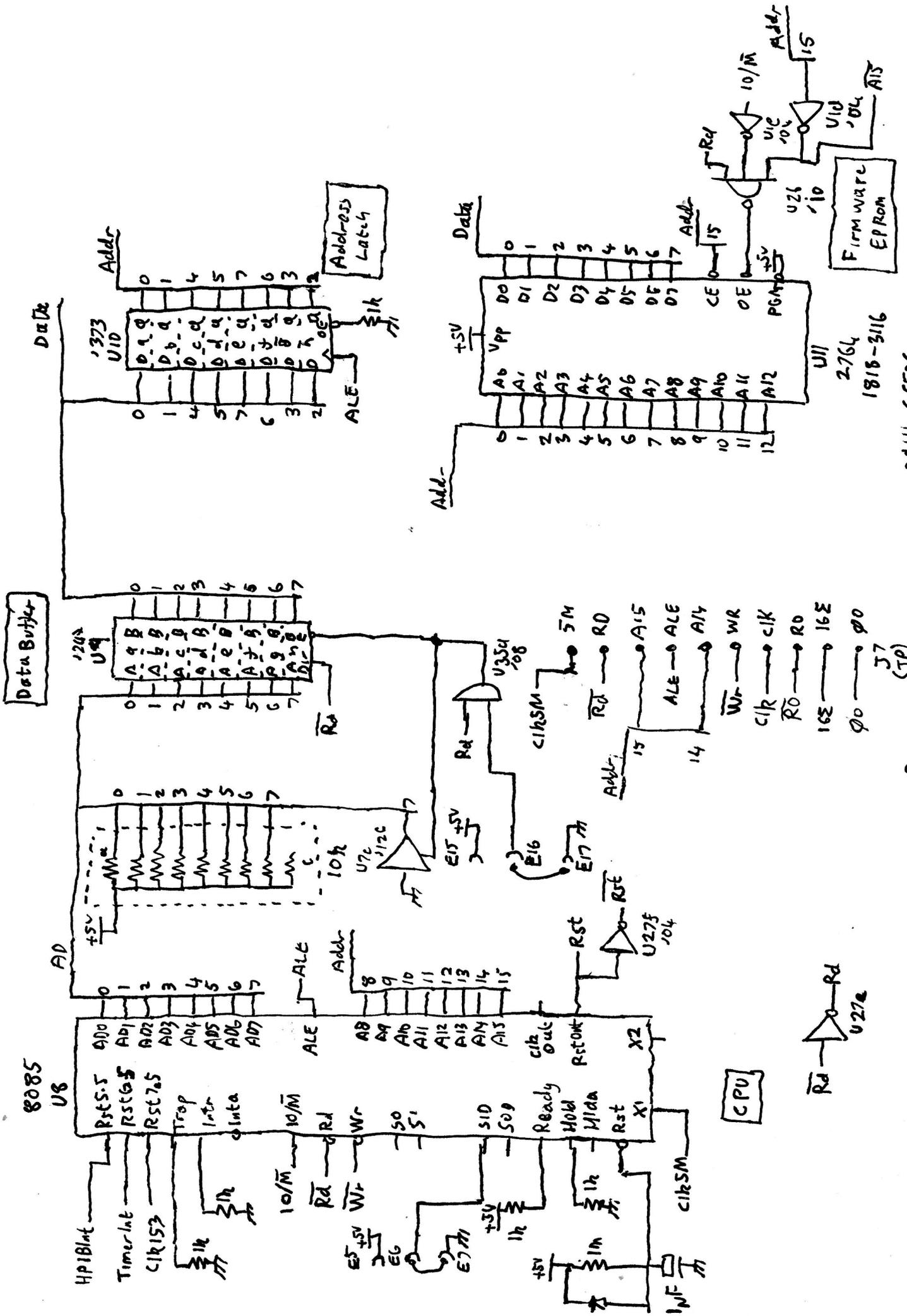
The schematic for the older version is on hpmuseum.net

Here are the diagrams for the later version along with the schematic for the platen PCB (09111-66502), believed to be used with both versions of the logic board.



0911-66506

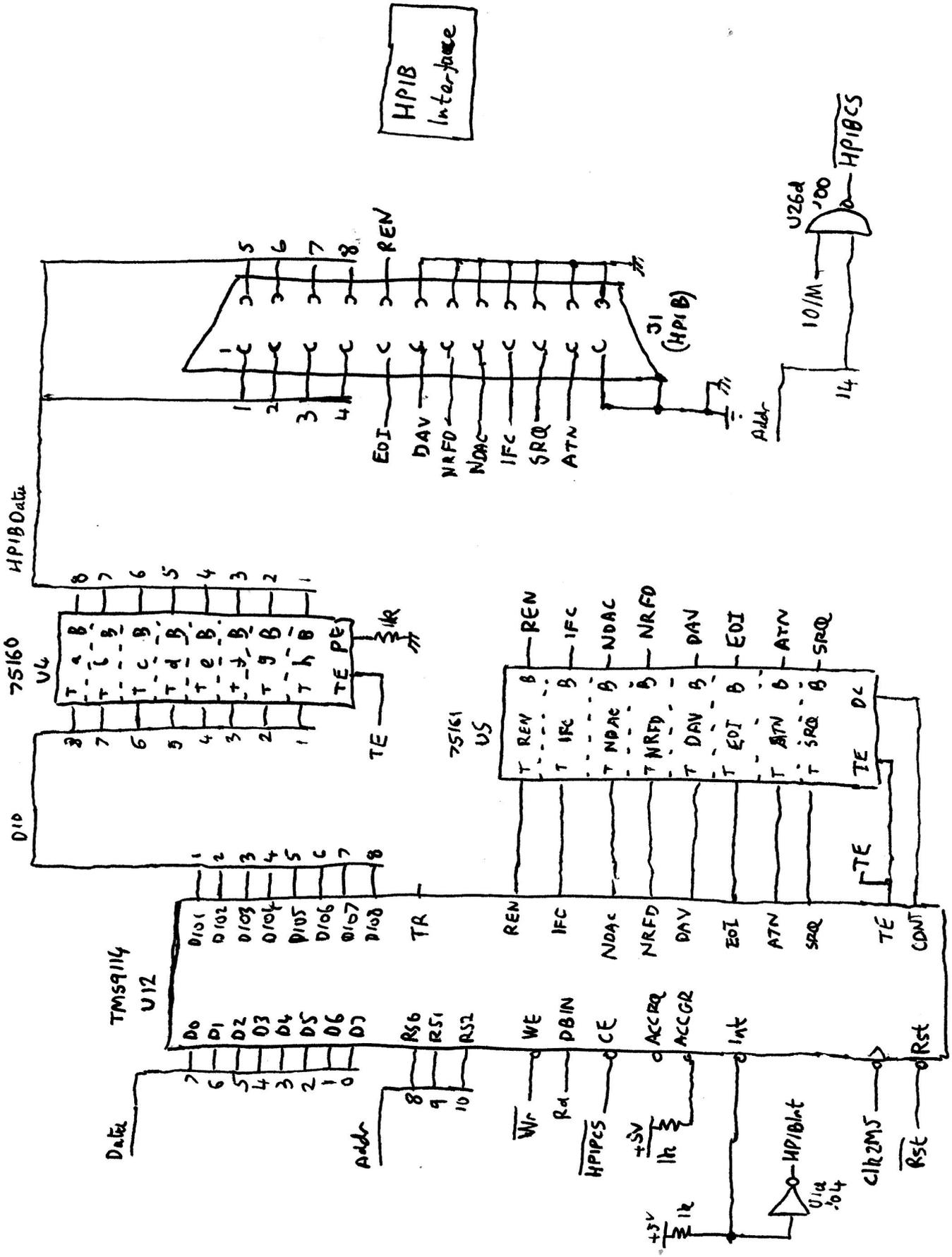
HP9111 Logic PCB Sheet ①

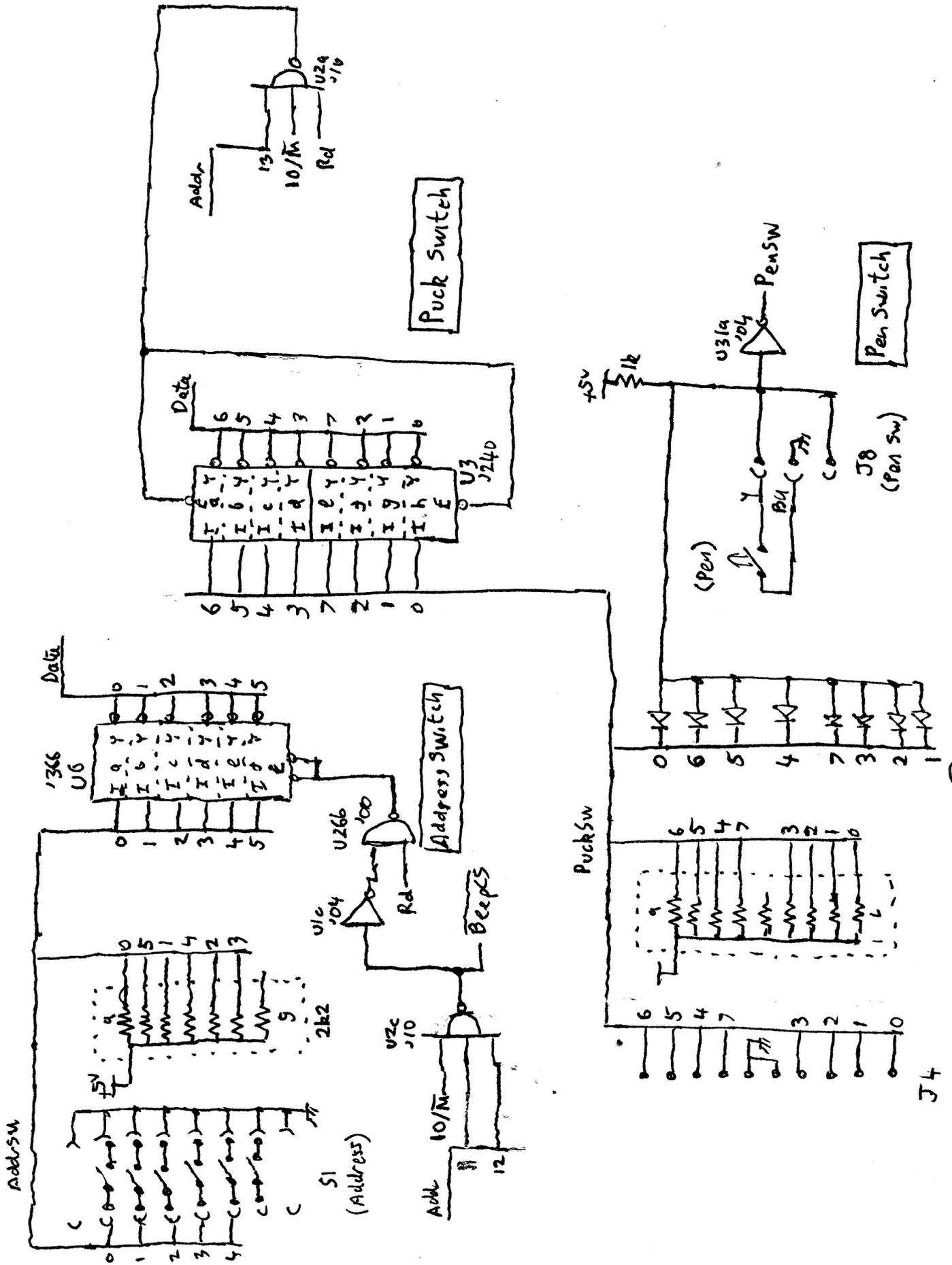


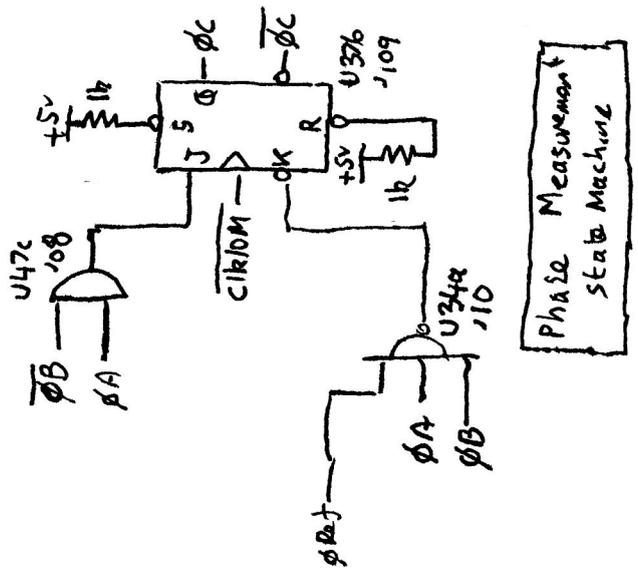
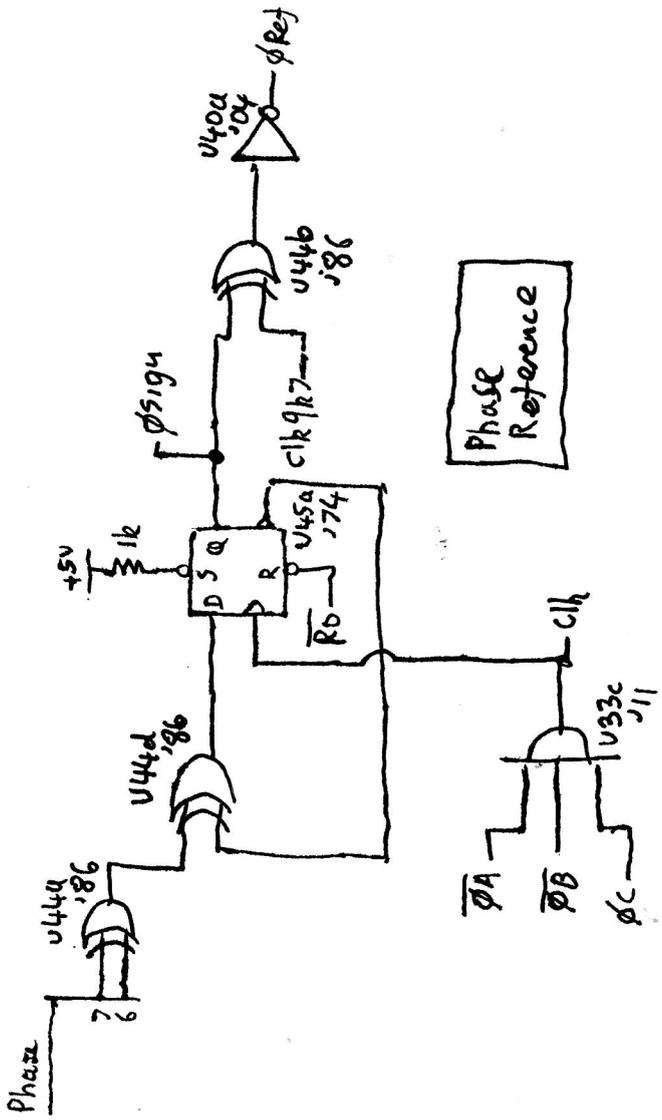
09111-66506

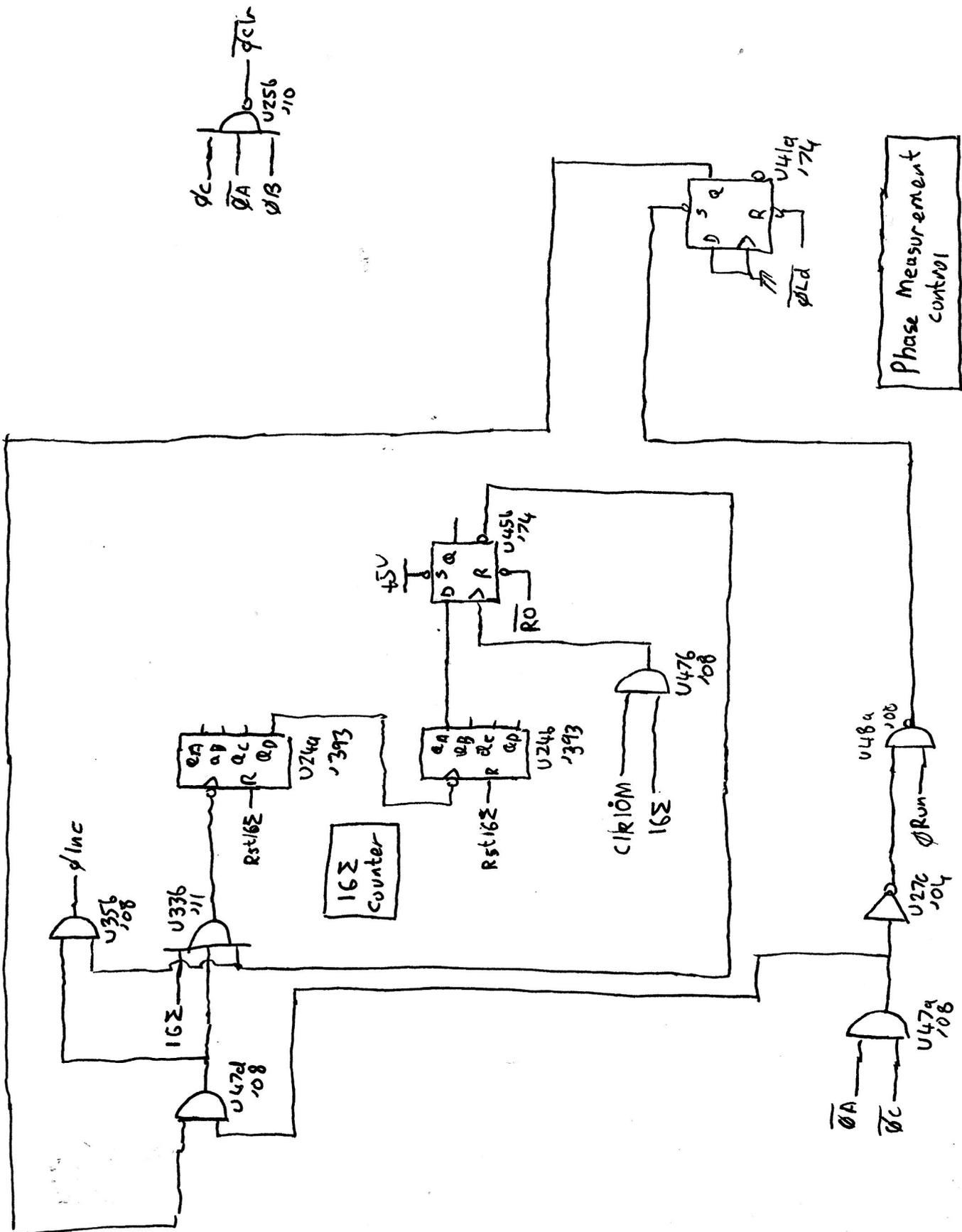
J7 (TP)

HP 9111 Logic PCB Sheet (2)





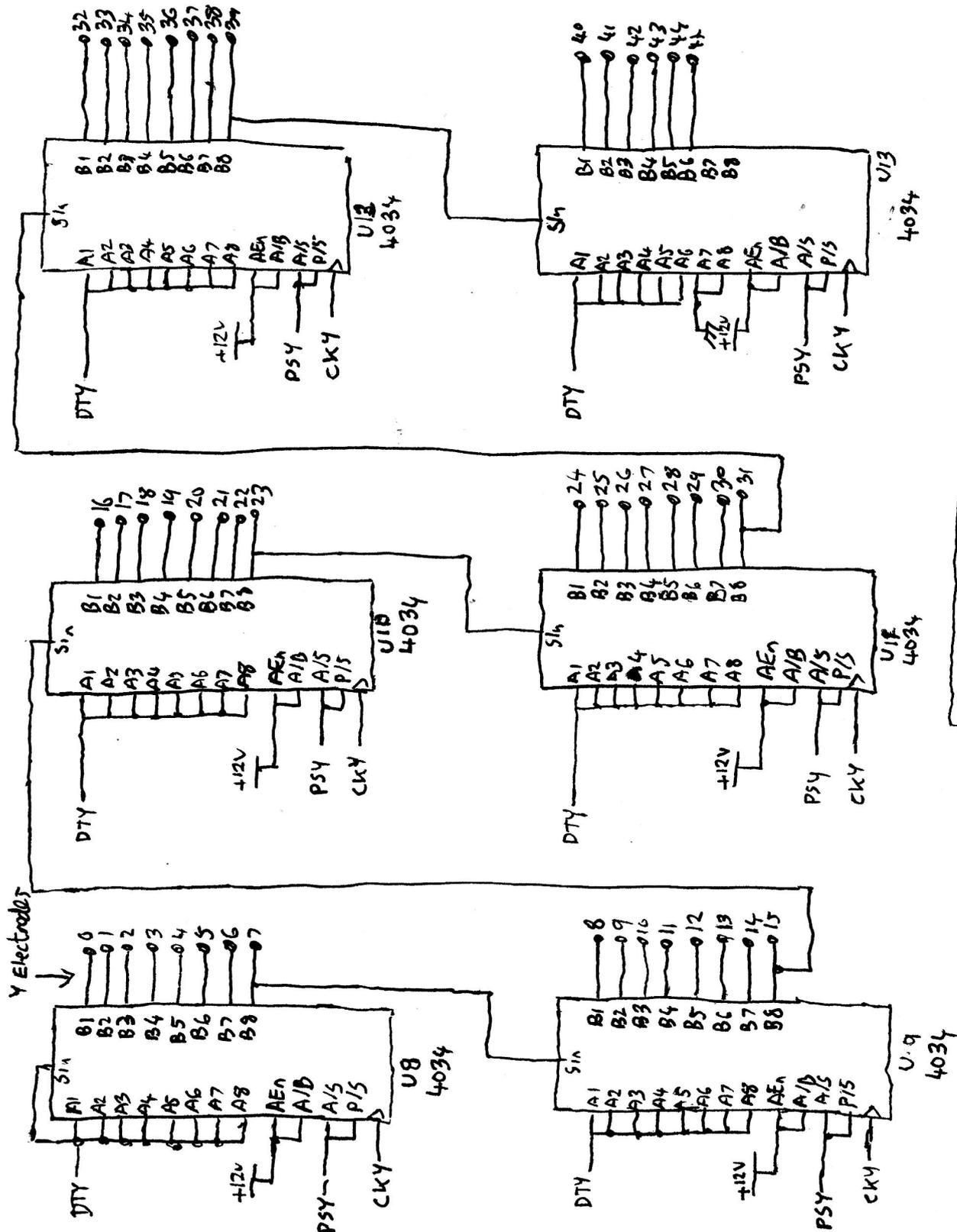




09111-66506

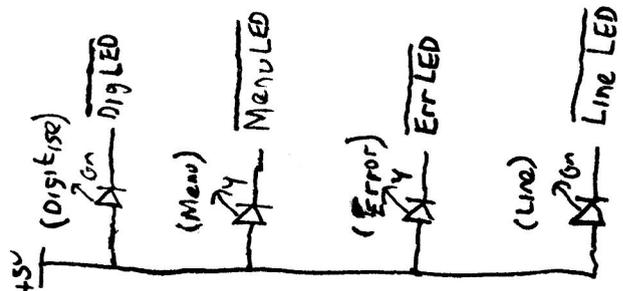
Sheet 10

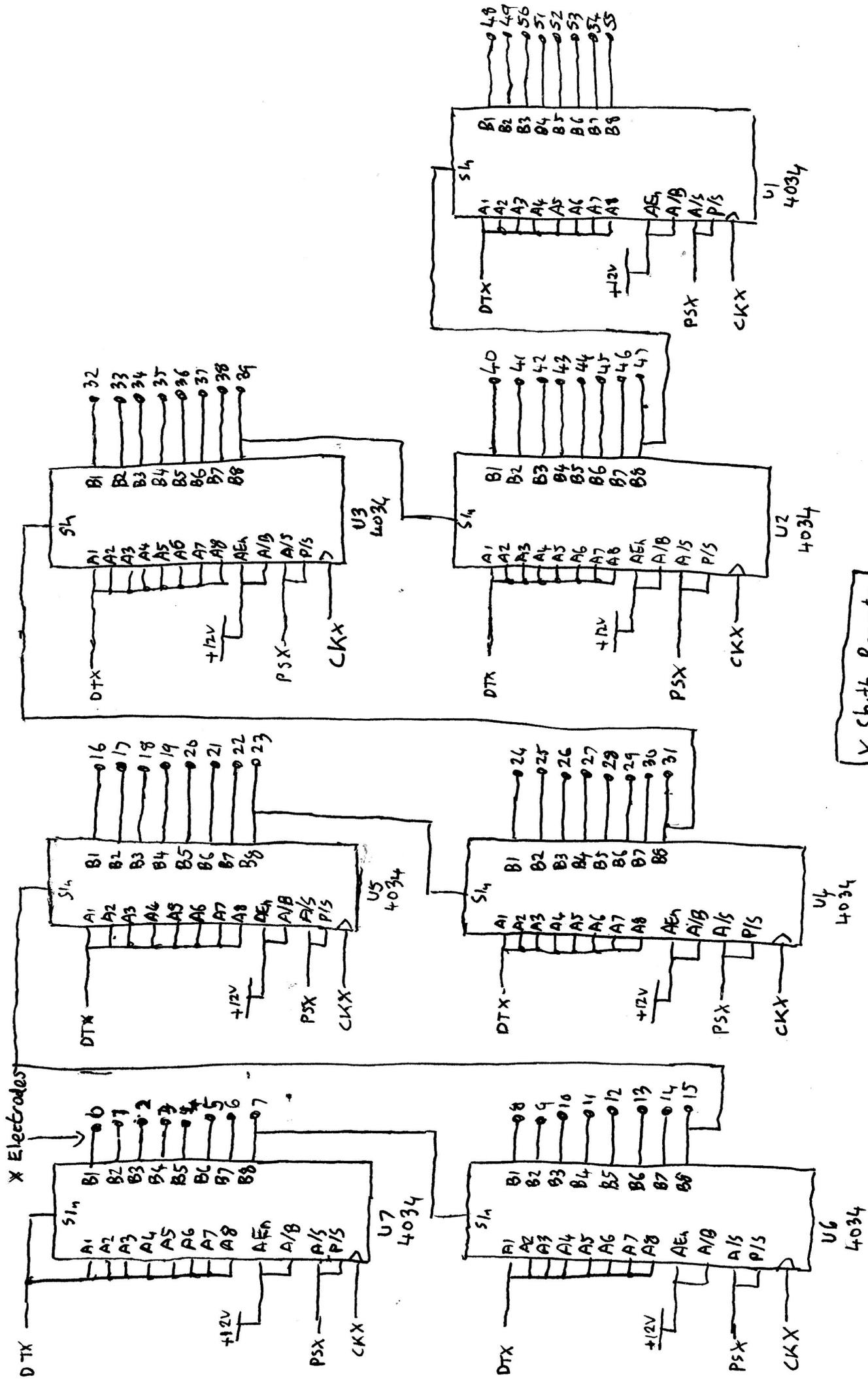
HP 9111 Logic PCB



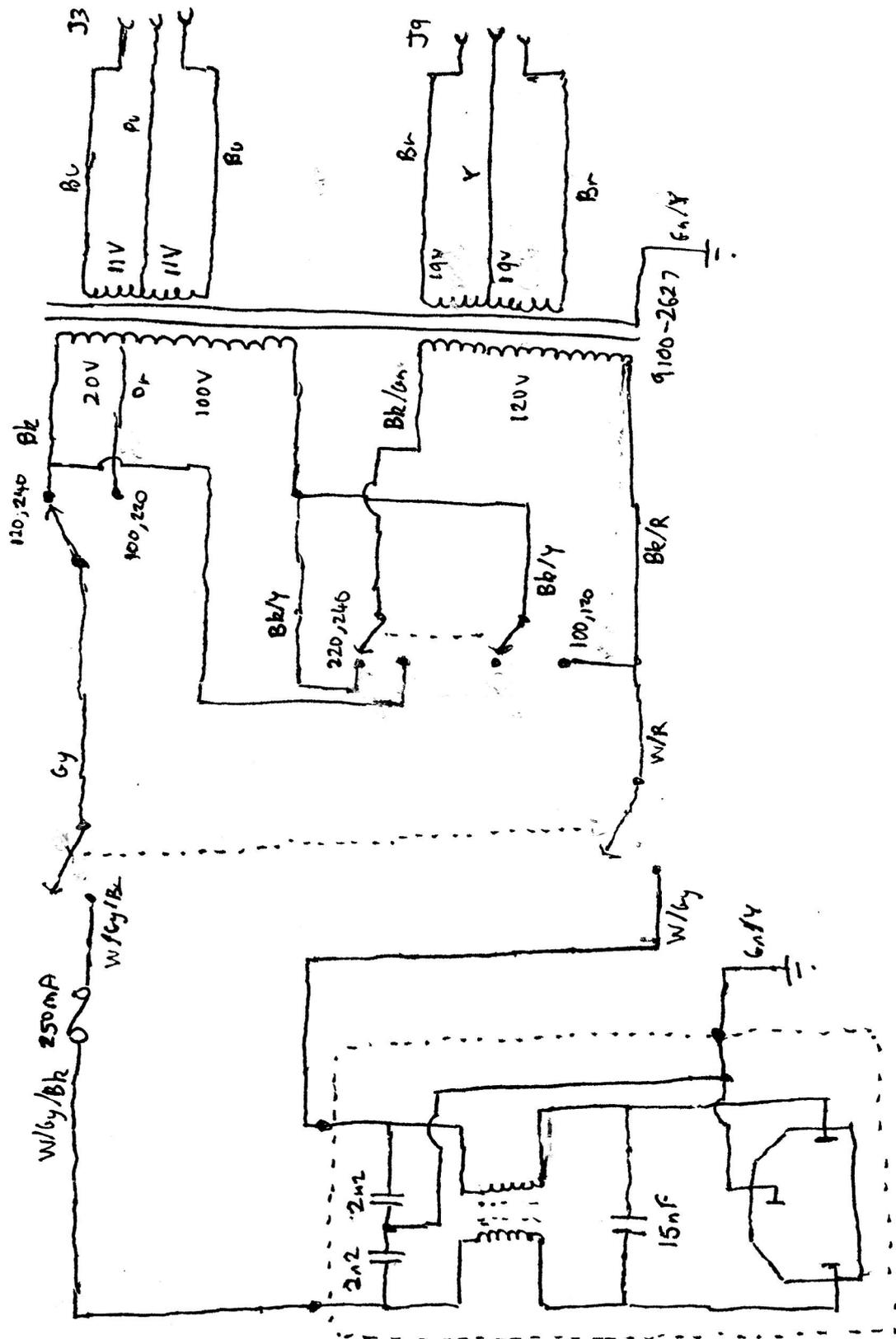
Y Shift Register

- DTX
- PSX
- CKX
- +5V
- +12V
- Line LED
- Error LED
- Menu LED
- Dig LED
- CKY
- PSY
- DTY





X Shift Register



HP9111A Transformer Wiring